

WEST Search History

DATE: Thursday, April 12, 2007

| Hide? | <u>Set</u> <u>Name</u> | <u>Query</u> | <u>Hit</u> <u>Count</u> |
|--------------------------|---------------------------------|---|----------------------------|
| | <i>DB=PGPB; PLUR=YES; OP=OR</i> | | |
| <input type="checkbox"/> | L19 | (delay with (sub adj net))[clm] | 1 |
| <input type="checkbox"/> | L18 | (waveform adj depression)[clm] | 1 |
| <input type="checkbox"/> | L17 | (((resistance and capacitance) or rc) same (sub adj net))[clm] | 1 |
| <input type="checkbox"/> | L16 | (test adj net)[clm] | 3 |
| <input type="checkbox"/> | L15 | (length with (sub adj net))[clm] | 1 |
| <input type="checkbox"/> | L14 | l11 and l12 | 3 |
| <input type="checkbox"/> | L13 | L12 and repeater[clm] | 1 |
| <input type="checkbox"/> | L12 | (empty near3 cell)[clm] | 111 |
| <input type="checkbox"/> | L11 | l3 and (layout or position or size or (test near3 terminal))[clm] | 64 |
| <input type="checkbox"/> | L10 | l4 and repeater[clm] | 1 |
| <input type="checkbox"/> | L9 | l3 and (test near5 signal)[clm] | 13 |
| <input type="checkbox"/> | L8 | l3 and (external near2 pin)[clm] | 3 |
| <input type="checkbox"/> | L7 | l3 and l4 | 6 |
| <input type="checkbox"/> | L6 | L5 and l3 | 1 |
| <input type="checkbox"/> | L5 | L4 and peripheral[clm] | 9 |
| <input type="checkbox"/> | L4 | ((("i/o" or "input/output" or "i-o" or "input-output" or io or inputoutput or "io" or "input output") adj2 area)[clm] | 64 |
| <input type="checkbox"/> | L3 | ((("i/o" or "input/output" or "i-o" or "input-output" or io or inputoutput or "io" or "input output") adj2 cell)[clm] | 204 |

END OF SEARCH HISTORY